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filed.

IN THE CLAIMS

NE
Please cancel claim 9 without prejudice or disclaimer.

Please amend claims 1, 5, 6, 8, 9, 10, 14, 15, 16, 19 and 23 as follows:

Sub C-1
B-1
1. (Twice Amended) A termination resistor circuit, provided in an interface circuit through which signals are transferred, comprising:

a first termination resistor block having a first plurality of transistors, the first plurality of transistors including at least one diode-connected transistor; and

a second termination resistor block having a second plurality of transistors, the second plurality of transistors including no diode-connected transistor, and wherein:

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

B2
Sub C-1
5. (Twice Amended) A termination resistor circuit provided in an interface circuit through which signals are transferred comprising:

a first termination resistor block; and

a second termination resistor block, wherein

said first termination resistor block differs from said second termination block by including at least one transistor whose gate is connected to its drain; and

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block, and

6. (Twice Amended) The termination resistor circuit as claimed in claim 4, wherein said first termination resistor block has first and second transistors and said first and second transistors are chosen to have a size for said first termination resistor block so that said first termination resistor block has a respectively chosen weight.

7. (Amended) The termination resistor circuit as claimed in claim 4, wherein said transistors of said first and second termination resistor blocks are chosen to be substantially equal in size for each of said first and second termination resistor blocks so that said first and second termination resistor blocks have the same weight.

8. (Twice Amended) The termination resistor circuit as claimed in claim 4, wherein said transistors for said first and second termination transistor blocks are chosen to have a size for each of said first and second termination resistor blocks so that said first and second termination resistor blocks have respectively chosen weights.

9. (Amended) The termination resistor circuit as claimed in claim 3, wherein a first transistor and a third transistor of said plurality of transistors of said first and second termination resistor blocks respectively are replaced by one common transistor.

10. (Twice Amended) A signal transmission system comprising:
a transmitting circuit for transmitting a signal;
a transmission line for transmitting the signal output from said transmitting circuit;
a receiving circuit for receiving the signal transmitted from said transmitting circuit

SEC-7
cont
B5-
through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block having a first plurality of transistors, the first plurality of transistors including at least one diode-connected transistor; and

a second termination resistor block having a second plurality of transistors, the second plurality of transistor including no diode-connected transistor, and wherein:

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

SEC-7
B6
14. (Twice Amended) A signal transmission system comprising:

a transmitting circuit for transmitting a signal;

a transmission line for transmitting the signal output from said transmitting circuit;

a receiving circuit for receiving the signal transmitted from said transmitting circuit through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block; and

a second termination resistor block, wherein:

said first termination resistor block differs from said second termination resistor block by including at least one transistor whose gate is connected to its drain; and

subc-1
Circuit
B6

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

15. (Twice Amended) The signal transmission system as claimed in claim 13, wherein first and second transistors are chosen to have a size for each of said first and second termination resistor blocks so that said first and second termination resistor blocks have respectively chosen weights.

subc-1
B7

16. (Amended) The signal transmission system as claimed in claim 13, wherein third and fourth transistors are chosen to be substantially equal in size for each of said first and second termination resistor blocks so that said first and second termination resistor blocks have the same weight.

subc-1
B8

19. (Twice Amended) A signal transmission system comprising:
a transmission line for transmitting a signal;
a receiving circuit for receiving the signal transmitted through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block having a first plurality of transistors, the first plurality of transistors including at least one diode-connected transistor; and

a second termination resistor block having a second plurality of transistors, the

second plurality of transistors including no diode-connected transistor, and wherein:

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.

23. (Twice Amended) A signal transmission system comprising:

a transmitting circuit for transmitting a signal;

a transmission line for transmitting the signal output from said transmitting circuit;

a receiving circuit for receiving the signal transmitted from said transmitting circuit through said transmission line; and

a termination resistor circuit connected to said transmission line and provided in an interface circuit through which signals are transferred, wherein said termination resistor circuit comprises:

a first termination resistor block having a first plurality of transistors, the first plurality of transistors including at least one diode-connected transistor; and

a second termination resistor block having a second plurality of transistors, the second plurality of transistors including no diode-connected transistor, and wherein:

said termination resistor circuit is switched between said first termination resistor block and said second termination resistor block.